



**MAHARAJA RANJIT SINGH PUNJAB TECHNICAL UNIVERSITY**  
**BADAL ROAD, BATHINDA-151001, PUNJAB (INDIA)**  
(Established by Punjab Govt. vide Punjab Act No. 5 of 2015 & Under Section 2(f) of the UGC Act at S.No.428)

Ref. No. MRSPTU/Purchase/17-18/ 6074 to 6079

Dated 18/8/17

(Regd.)

To

As per list attached.

**Sub: - Quotations for Purchase of Lab Equipment for DE Lab of ECE Deptt., GZSCCET, Bathinda.**

Dear Sir,

01. You are requested to quote the rates for the items shown in the Appendix 'A' separately attached to this letter.

02. Following shall be taken note of while quoting the rates:-

- The rates will be F.O.R. Store Yard, MRSPTU, Bathinda.
- Rate of ST/VAT <sup>9%</sup> if extra must be mentioned clearly.
- Quotation received later than due date are liable to be ignored/rejected.
- Quotation must be submitted on letter head of the firm with all particular, any other format will not be acceptable.
- The quality of the items to be quoted should be best available in the market.
- The envelope must bear the word:-

**"QUOTATION FOR Purchase of Lab Equipment for DE Lab of ECE Deptt., GZSCCET, Bathinda.**

Enquiry No. As above dated \_\_\_\_\_  
Due on 07-09-2017

**IMPORTANT:** -In the absence of information as required under 2(f) above, if an envelope is received & opened inadvertently, the university will not be responsible and the quotation may not be considered even if quoting lowest rates.

03. The quotations duly completed should be in the name of Registrar and should reach the **office of the Deputy Registrar (S&P), Maharaja Ranjit Singh Punjab Technical University, Bathinda by 07-09-2017 up to 03:30 PM** and same shall be opened at **04:00 PM** on same date. Your representative may be present during the opening of the quotations.

Deputy Registrar (S&P),  
MRSPTU, Bathinda.

Copy to:-

- ✓ Incharge, Computer Centre, to upload a copy on university & college websites.
- Head, ECE Deptt.
- Master File

P.T.O.

## **TERMS & CONDITIONS**

1. **VICE CHANCELLOR RIGHTS:**

Vice Chancellor, MRSPTU, Bathinda reserves the rights of acceptance or rejection of any or all quotations. The discretion for increasing or decreasing of the quantities also rests with him. MRSPTU also not bind itself to accept the lowest price. In case of any dispute, the decision of Vice Chancellor, MRSPTU, Bathinda will be final & binding.

2. **VALIDITY OF QUOTATIONS:-**

Quotations will be considered valid for 03 months from the date of quotation.

3. **CORRESPONDANCE:**

No correspondence regarding acceptance/rejection of a quotation will be entertained.

4. **SAMPLE/BRAND/MAKE/WEIGHT:-**

Sample where asked for, will invariably be made available and sent along with the quotations. However, Brand/Make/Weight etc. must be mentioned clearly in the quotations. Technical literature/pamphlet should also be enclosed.

5. **REJECTION:-**

Quotation not conforming to the set procedure as above will be rejected.

6. **DISCOUNT/REBATES:-**

A Special discount/rebate wherever admissible keeping in view that the supplies are being made for education purpose in respect of Public Institution of national importance may please be indicated.

7. **DELIVERY PERIOD:-**

Minimum Period for delivery/job completion should be mentioned clearly.

8. **GUARANTEE/WARRANTY:-**

Guarantee/Warranty should be mentioned clearly.

MRSPTU shall not be held responsible for any postal delay in sending or late receipt of quotation. Quotation should be free from corrections & erasures.





**Appendix-'I'**

Sr. No	Description of Item	Qty.
01.	<p><b>Digital Lab Trainer (With Bread Board)</b></p> <p><b>Technical Specifications:</b> DC Regulated Power Supply Output voltages : One fixed DC regulated power supply of +5V/1 Amp : One fixed DC regulated power supply of +5V/1 Amp : One variable DC regulated power supply of + 3 - +12V/ 500mA Load regulation : + 0.2% Line regulation : + 0.05% Ripple : Less than 3mV RMS Protection : Short circuit &amp; over load protected Pulse Generator Operating modes : Square Frequency range : 1Hz to MHz in 6 Steps (3V ~ 15V) (CMOS), 5V (TTL) Duty Cycle : 50%, TTL/CMOS output Switches/LED/Display 2 Nos. Push to on Pulsar Switches 8 Nos. toggle switches for both TTL &amp; CMOS mode 8 Nos. LED Display (TTL/CMOS Mode) 3 Nos. Seven Segment Display Logic Probe Logic level indicator for TTL/CMOS (7 Seg.) Power requirement : 220 VAC + 10%, 50 Hz Weight: 3.0 Kg Approx. Dimensions (mm) : 330 (L) x225 (B) x 75(H) Standard Accessories: 5 mtr. hookup wire, 10 nos. Patch Chords With 2mm banana plug on one end, Power Chord &amp; Instruction Manual Bread Board Strips : 2 Nos vertically common strips (640 Tie points esch), 4 Nos horizontally common strips (100 Tie points each)</p>	05
02.	<p><b>Digital iab Modules</b></p> <p>i) Logic Gate ii) Universal Gate – NAND/NOR Gate iii) Code Conversion (Binary to gray and gray to binary) iv) Binary Adder/Subtractor v) Encoder/Decoder (8 to 3 Line Encoder, 3 to 8 Line Decoder) vi) Multiplexer/Demultiplexer (4-1 Line Multiplexer, 1-4 Lin Demultiplexer) vii) Flip-Flops (R-S, D, J-K, T Flip-Flops) viii) BCD to 7 Seven Segment Decoder ix) 4 Bit Adder &amp; Subtractor Circuits using IC 7483</p> <p>Specification: To study &amp; verify truth tables of two bit half adder , two bit half subtractor, three and four bit full adder and full subtractor, Instrument comprises of DC Regulated Power Supply 5V/150mA, four Logic inputs selectable using SPDT switches, four output LED Indicators, circuit diagram Printed &amp; connections brought out at sockets on the front panel</p>	05 05 05 05 05 05 05 05 05

Specifications checked & verified.

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Prof & Head,  
Electronics & Communication Engg. Deptt.  
GZSCCET, Bathinda

*D*  
16/08/17

*Deep*  
16/08/17